

PHOTO LINK TRANSMITTER SPECIFICATION

TOTAL PAGE: 8 PAGE: 1 **REVISION:** 1.0

● DEVICE NUMBER: BFTX-1001/HP

| SHEET DATE | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | | CONTENTS |
|---------------|-----|-----|-----|-----|-----|-----|-----|-----|--|------------------|
| 2003.06.04 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | 1.0 | | Initial Released |
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| 2003.06.04 | 2003.06.04 |
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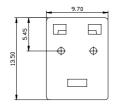
• Features:

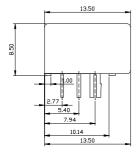
- 1. Conform to EIAJ Standard CP-1201 (For Digital Audio Interface including Fiber Optic inter-connections).
- 2. TTL interface.
- 3. LED is driven by differential circuit.
- 4. +5V single power supply.
- 5. High speed signal transmission (12.5M NRZ signal).
- 6. ESD tolerance IC>8KV.
- 7. Housing heat deflection temperature 290°C(@1.8MPa/ISO75).
- 8. Compatible Toshiba Toslink mini-package type.

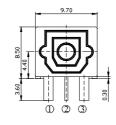
Applications:

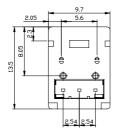
- 1. Digital audio equipment:PC sound cards, Notebook and Portable devices.
- 2. Navigation system.

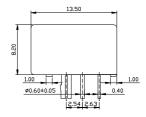
Outline Dimensions





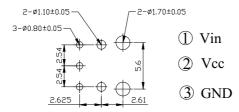






- ① Vin
- ② Vcc
- 3 GND

• Recommended drilling as viewed from the soldering face



NOTES: Tolerance is ± 0.3 mm unless otherwise noted.

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● Absolute Maximum Ratings (Ta=25°C)

| Parameter | Symbol | Rating | Unit | |
|-----------------------|--------|--------------------|----------------|--|
| Power Dissipation | Pd | 100 | mw | |
| Supply voltage | Vcc | -0.5 to + 7 | V | |
| Input voltage | Vin | -0.5 to Vcc $+0.5$ | V | |
| Operating temperature | Topr | -20 to + 70 | | |
| Storage temperature | Tstg | -30 to + 80 | $^{\circ}\! C$ | |
| Soldering temperature | Tsol | 260 For 5sec | | |

● Electro-Optical Characteristics (Ta=25°C)

| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|--------------------------|-----------|--|------|------|------|------|
| Peak wavelength | λр | | | 660 | | nm |
| Operating supply voltage | Vcc | | 4.75 | 5.0 | 5.25 | V |
| Data rate | T | NRZ code | | | 12.0 | Mbps |
| Transmission Distance | D | Using All Plastic Fiber (970/1000μm) and TORX179 | 0.2 | | 5 | m |
| Optical power output | Pc | Refer to Fig. 1 | -21 | -17 | -15 | dBm |
| Dissipation current | Icc | Refer to Fig. 2 | | 8 | 13 | mA |
| High level input voltage | V_{iH} | Refer to Fig. 2 | 1.5 | | Vcc | V |
| Low level input voltage | V_{iL} | Refer to Fig. 2 | 0 | | 0.8 | V |
| Low→High delay time | t_{PLH} | Refer to Fig. 3 | | | 180 | |
| High→Low delay time | t_{PHL} | Refer to Fig. 3 | | | 180 | na |
| Pulse width distortion | ∆tw | Refer to Fig. 3 | -25 | | +25 | ns |
| Jitter | ∆tjr | Refer to Fig. 3 | | 4 | 25 | |

● Mechanical Characteristics (Ta=25°C)

| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|---------------------|--------|-----------------------------------|------|------|------|------|
| Insertion Force | | *1 | | | 40 | N |
| Withdrawal Force | | *1 | 6 | | 40 | IN |
| Torque for Self-Tap | | Using self-tapping screw (M3 x 8) | 60 | | 100 | N-cm |

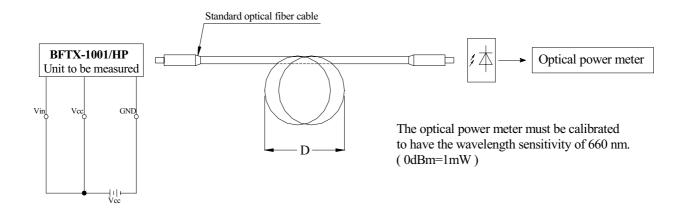
^{*1:} Using standard optical fiber cable ($970/1000 \mu m$)

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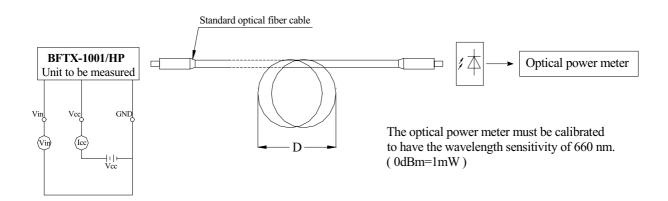
• Fig.1 Measuring Method of Optical Output Coupling with Fiber.



Notes: (1) Vcc=5.0V (State of operating)

(2) To bundle up the standard fiber optic cable, make it into a loop with the diameter D=10cm or more.

• Fig.2 Measuring Method of Input Voltage and Supply Current.



Input conditions and judgment method Supply Current.

| Conditions | Judgment method | | |
|-------------------|--|--|--|
| Vin=2.1V or more | -21 dBm \leq Pc \leq -15 dBm, Icc=13mA or less | | |
| Vin=0.8 V or less | Pc≤-36 dBm, Icc=13mA or less | | |

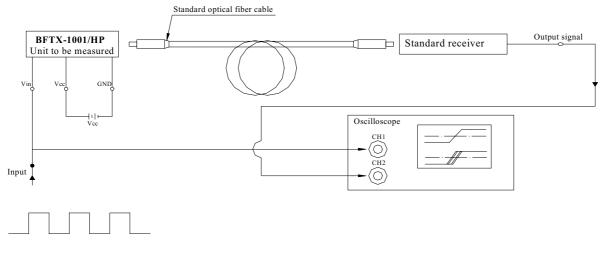
Notes: Vcc=5.0V (State of operating).

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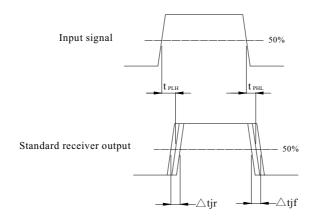
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• Fig.3 Measuring Method of Pulse Response and Jitter.



12Mbps NRZ code, duty 50%



Test item

| Test item | Symbol | Test item |
|---------------------------|------------------|--|
| Low→High pulse delay time | t _{PLH} | Refer to the above prescriptions. |
| High→Low pulse delay time | t_{PHL} | Refer to the above prescriptions. |
| Pulse width distortion | ∆tw | $\triangle tw = t_{PHL} - t_{PLH}$ |
| Low→High Jitter | ∆tjr | Set the trigger on the rise of input signal to measure the jitter of the rise of output. |
| High→Low Jitter | ∆tjf | Set the trigger on the fall of input signal to measure the jitter of the fall of output. |

Notes:

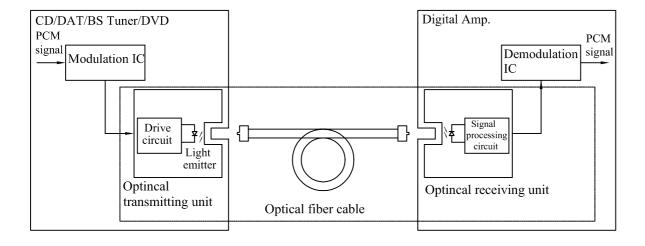
- (1) The waveform write time shall be 4 seconds. But do not allow the waveform to be distorted by increasing the brightness too much.
- (2) Vcc=5.0V (State of operating)
- (3) To probe for the oscilloscope must be more than $1M\Omega$ and less than 10pF.

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System Configuration Example:



• Application Circuit:

Fiber optic connector insertion side

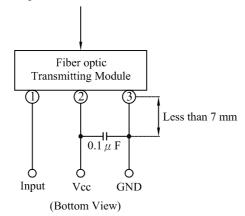


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RELIABILITY TEST

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PAGE:

| Classification | Test Item | Reference Standard | Test Conditions | Result | |
|----------------|---------------------|--------------------|---------------------------------|--------|--|
| Classification | | | | Kesuit | |
| | Operation Life | MIL-STD-750:1026 | Connect with a power Vcc,Vin=5V | 0/20 | |
| | | MIL-STD-883:1005 | Ta=Under room temperature | 0/20 | |
| | III' 1 TD | JIS C 7021 :B-1 | Test time=1,000hrs | | |
| | High Temperature | MIL-STD-202:103B | Ta=85°C±5°C | 0 (2.0 | |
| Endurance | High Humidity | JIS C 7021 :B-11 | RH=90%-95% | 0/20 | |
| Test | Storage | | Test time=240hrs | | |
| | High Temperature | MIL-STD-883:1008 | High Ta=105°C±5°C | 0/20 | |
| | Storage | JIS C 7021 :B-10 | Test time=1,000hrs | 0/20 | |
| | Low Temperature | JIS-C-7021 :B-12 | Low Ta=-55°C±5°C | 0/20 | |
| | Storage | J15-C-7021 .B-12 | Test time=1,000hrs | 0/20 | |
| | Temperature Cycling | MIL-STD-202:107D | -55°C ~ 25°C ~ 105°C ~ 25°C | | |
| | | MIL-STD-750:1051 | 30min 5min 30min 5min | 0/20 | |
| | | MIL-STD-883:1010 | Test Time=10cycle | 0/20 | |
| | | JIS C 7021 :A-4 | | | |
| | Thermal Shock | MIL-STD-202:107D | -55°C±5°C ~ 105°C±5°C | | |
| | | MIL-STD-750:1051 | 10min 10min | 0/20 | |
| | | MIL-STD-883:1011 | Test Time=10cycle | | |
| Environmental | Solder Resistance | MIL-STD-202:201A | T.sol=260±5°C | | |
| Test | | MIL-STD-750:2031 | Dwell Time=5±1sec. | 0/20 | |
| | | JIS C 7021 :A-1 | | | |
| | Solder ability | MIL-STD-202:208D | T.sol=230±5°C | | |
| | · | MIL-STD-750:2026 | Dwell Time=5±1sec. | 0/20 | |
| | | MIL-STD-883:2003 | | 0/20 | |
| | | JIS C 7021 :A-2 | | | |
| | Lead Bending Stress | MIL-STD-750:2036 | 0°~90°~0°bend, 3 cycles | 0/20 | |
| | | JIS C 7021 :A-11 | Weight 250g | 0/20 | |

JUDGMENT CRITERIA OF FAILURE FOR THE RELIABILITY

| Parameter | Symbol | Measuring conditions | Judgement criteria for failure |
|----------------------|--------|----------------------|--------------------------------|
| Optical power output | Pc | Vcc,Vin=5V | -21dBm~-15dBm |
| Dissipation current | Icc | Vcc,Vin=5V | Over Ux2 |

Note: 1.U means the upper limit of specified characteristics. S means initial value.

2.Measurment shall be taken between 2 hours and after the test pieces have been returned to normal ambient conditions after completion of each test.

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PACKAGING DIMMENSIONS

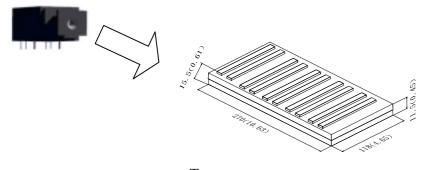
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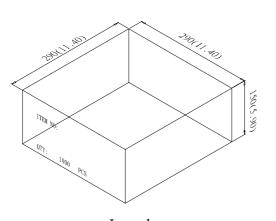
Package Method:(unit:mm)



100 Pcs / Tray



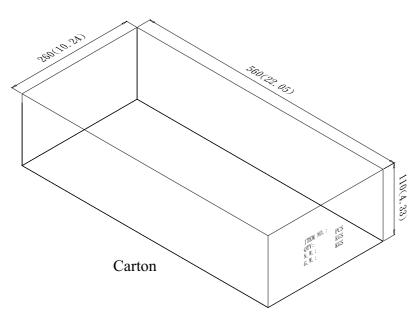
Tray



10 Tray / Inner box



Inner box



2 Inner box / Carton

NOTES : Tray : Tolerance is \pm 5 mm unless otherwise noted.

Inner box : Tolerance is \pm 10 mm unless otherwise noted. Carton : Tolerance is \pm 10 mm unless otherwise noted.